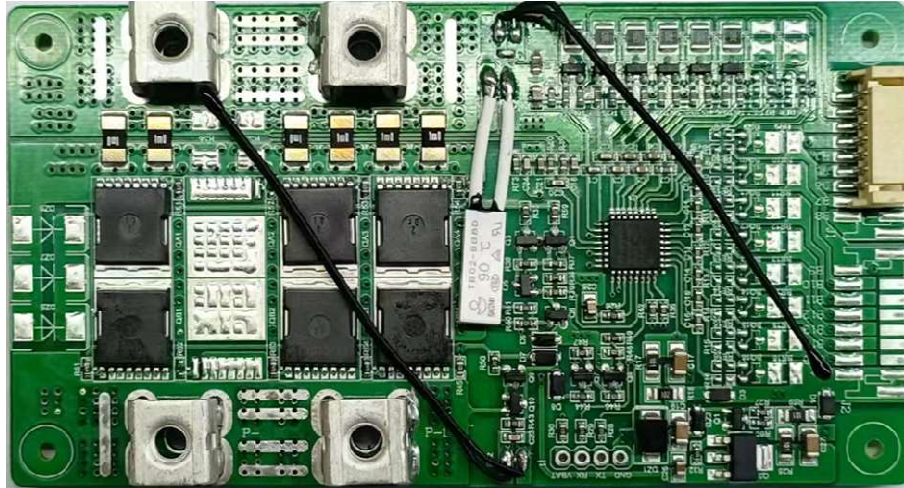


Specification for P/N: PCM-LI21.6V70A

Features:

- It has multifunctional safety protection of over charge, over discharge, over current, short-circuited, temperature.
- It has balancing function under the state of charging.
- The FET temperature is lower than 90°C after discharging for over 1 hour at 70A under ambient temperature 30°C
- Not support connection in series and parallel

No	Item	Protection Condition	Delay Time	Release Condition	
1	Over Charge Voltage	4.25±0.10V	1s	4.15±0.10V	
2	Under Discharge Voltage	2.75±0.10V	1s	3.00±0.10V	
3	Over Current	OCD1 Threshold	200A±30A	500ms	Cut load, auto recover
		Short circuit protection	600A±30A	50-600us	Cut load, auto recover
4	Over temperature NTC (1 built in, 2 external)	Over Temperature for Charging (OTC)	75±3°C	5s	55±3°C
		Over Temperature for Discharging (OTD)	75±3°C	5s	55±3°C
		MOSFET Over Temperature Threshold (OTF)	90±5°C	5s	65±15°C
		Under Temperature for Charging (UTC)	0±3°C	5s	5±3°C
		Under Temperature for Discharging (UTD)	-20±3°C	5s	-15±3°C
5	Cell Balancing	Cut in voltage	4.2±0.10V		
		Balance Current	68±10mA		
6	Dimension	LxWxT: 120x65x22 mm			
7	Impedance	≤10mΩ, from B- to P- and B+ to P+			
8	Current Consumption	≤ 300uA, operation mode			
9	Temperature	Working	-20 to 85°C		
		Storage	-40 to 125°C		



Port	Terminal No	Descriptions
B-,B-1	B-,B-1	Negative pole for main circuit to be connected with negative pole of Cell1
J1	B-	Negative terminal for cell1
	B1	Positive terminal for Cell1 and negative terminal for Cell 2
	B2	Positive terminal for Cell2 and negative terminal for Cell 3
	B3	Positive terminal for Cell3 and negative terminal for Cell 4
	B4	Positive terminal for Cell4 and negative terminal for Cell 5
	B5	Positive terminal for Cell5 and negative terminal for Cell 6
	B6	Positive pole for main circuit to be connected with positive pole of Cell 6
T1	T1	Terminal for MOSFET temperature detection device
RT1,RT2	RT1,RT2	Terminal for battery temperature detection device
P-, P-1	P-, P-1	Negative terminals for charge and discharge
<p>Note: The connection between cells and PCB should be followed this order: B-.....→B+, otherwise it will cause potential damage to the BMS if not go by this connection order.</p>		